

8A, 650V Silicon Carbide Schottky Diode

Features

- High-Frequency Operation
- Zero Reverse Recovery Current
- Temperature-Independent Switching
- Extremely Fast Switching
- Plastic package has underwriters Laboratory Flammability Classification 94V-0
- Halogen-free according to IEC 61249-2-21

Applications

- Boost Diodes in PFC or DC/DC stages
- LED Lighting Power Supplies
- Power Factor Correction

Mechanical Data

- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 sec
- Shipped 50 units per plastic tube

Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted)					
Parameter	Symbol	GS08D065SI	Unit		
Maximum repetitive peak reverse voltage	Vrrm	650	V		
Working peak reverse voltage	VRWM	650	V		
Maximum DC blocking voltage	VDC	650	V		
	Tc=25°C		26		
Maximum average forward rectified current	Tc=135°C	lf(AV)	11.7	А	
	Tc=152°C		8		
Peak forward surge current, tp=10ms,Half Sir	IFSM	64	А		
Dower dissinction	Tc=25°C	Dut	39	w	
Power dissipation	Tc=110°C	- Ptot	17		
Operating junction temperature range	TJ	-55 to +175	°C		
Storage temperature range	Тѕтс	-55 to +175	°C		







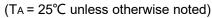
Electrical Specifications(TA=25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Тур	Max	Unit	
Forward drap valtage	VF	IF=8A, TJ=25°C	1.40	1.65	v	
Forward drop voltage		IF=8A, TJ=175°C	1.80	2.40		
Poverse leakage current @rated Vp	IR	V _R =650V, TJ=25℃	2	50		
Reverse leakage current @rated VR	IR	V _R =650V, TJ=175℃	10	180	μA	
Total capacitive charge	Qc	VR=400V, IF=8A, TJ=25°C	28	-	nC	
Total capacitance	С	Vr=400V, Tj=25°C, f=1MHz	42	-	pF	

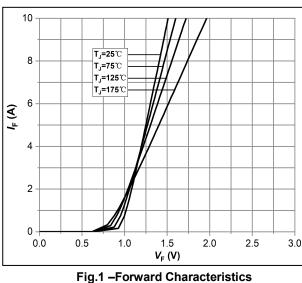
Thermal-Mechanical Specifications (TA=25°C unless otherwise noted)				
Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Case	Rejc	3.80	-	°C /W



GS08D065SI GOOD-ARK Electronics

Ratings and Characteristics Curves





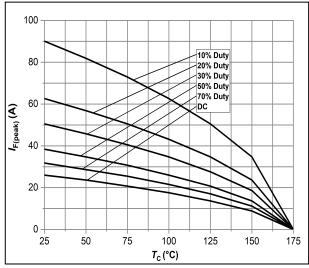


Fig.3 –Current Derating

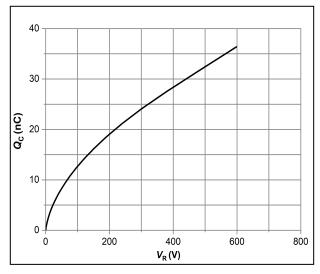
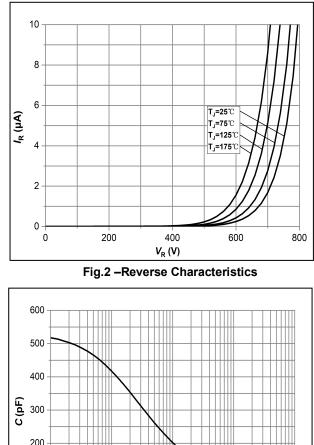


Fig.5 – Total Capacitance Charge vs. Reverse Voltage



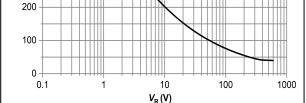


Fig.4 – Capacitance vs. Reverse Voltage

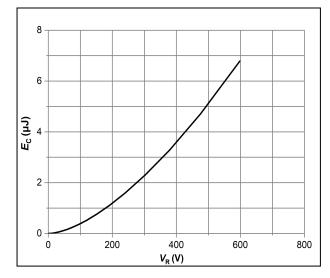
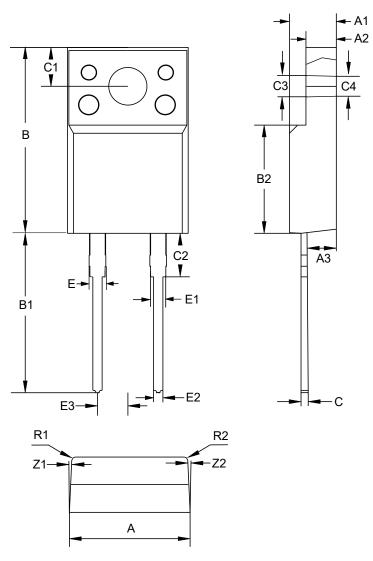


Fig.6 – Typical Capacitance Stored Energy



Package Outline Dimensions (Unit: millimeters)

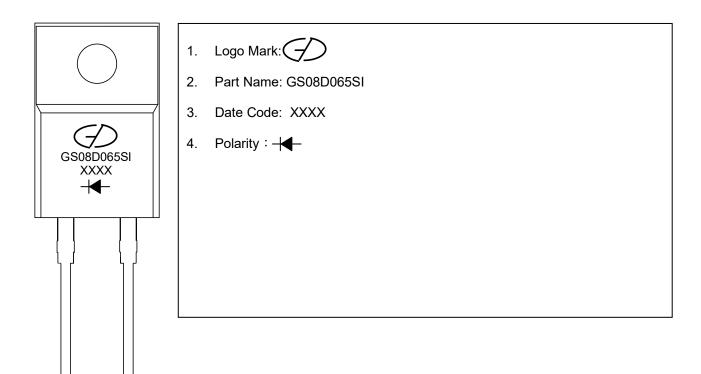
ITO-220AC



ITO-220AC							
	Min.	Nom.	Max.		Min.	Nom.	Max.
А	9.9	10.1	10.3	C3	3.0	3.2	3.4
A1	4.6	4.7	4.8	C4	3.0		
A2	2.44	2.54	2.64	E	1.15	1.35	1.55
A3	2.25	2.45	2.65	E1	1.17	1.27	1.37
В	15.5	15.8	16.1	E2	0.7	0.8	0.9
B1	13.25	13.55	13.85	E3	2.44	2.54	2.64
B2	9.0	9.2	9.4	R1		0.3	
С	0.5	0.6	0.7	R2		0.3	
C1	3.1	3.3	3.5	Z1		3°	
C2	3.0	3.3	3.6	Z2		3°	



Marking Outline



Revision History

Document Version	Date of release	Description of changes
Rev.A	2022.06.16	Preliminary Datasheet



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